* **List the access method on Memory?**

**Answer:**

1. **sequential access: It is reading or writing data records in a sequential order, meaning that it is not allowed to read a specific file without reading the previous files of it and also writing**
2. **Direct access: A technology that provides an intranet connection to computers when they are connected to the Internet. Unlike many traditional technologies, which must start and end with user permission, DirectAccess connections are designed to automatically connect as soon as your computer connects to the Internet.**
3. **Random Access: Each address in the memory has one address, and access to the designated site is separate from the address of other processes, and any random location can be chosen, processed, and accessed directly.**
4. **Associative: That enables one to make a comparison of desired bit location instead of the address and this method is different from the previous methods, each site has its own work and thus the recovery time is fixed and separate from the site**

* **Write notes about “Word” on Memory?**

**Answer:**

**The data space in the cell equal to number of bits ( word length of CPU ) then the corresponding address space is called as Word Address.**

* **Write notes about Memory hierarchy?**

**Answer:**

**Can be summed up the design constraints on a computer's by 3Q,, How much?How fast?How cost?**

**If the capacity is there, we can deal with it with a very large area and specify the applications that we want to develop,In order to achieve a large rate of utilization of the memory, you must work with the processor very accurately,Take into account the cost of the memory, be small with other components.**

1. **Faster access time,greater cost per bit**
2. **Greater capacity,smaller cost per bit**
3. **Greater capacity,slower access time**

**The designer would like to use memory technologies that provide for large capacity memory, both because the capacity is needed and because the cost per bit is low. however, to meet performance requirements, the designer need to use expensive, relatively lower capacity memories with short access times.**